

PTO/SB/08a (08-03)

Approved for use through 07/31/2006. OMB 0651-0031

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Substitute for form 1449A/PTO		Complete if Known		
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>		Application Number	10/663,911	
		Filing Date	09/17/2003	
		First Named Inventor	FRAMPTON E ELLIS	
		Art Unit	2153	
		Examiner Name		
(Use as many sheets as necessary)				
Sheet	1	of	1	
			Attorney Docket Number	081498-0305710

U. S. PATENT DOCUMENTS

**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document <sup>2</sup>	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	Type of Reference
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				

Examiner Signature	/l ashanya Nash/ (07/14/2010)	Date Considered
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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet

1

of

1

Attorney Docket Number

081498-0305710

Complete if Known

Application Number

10/663,911

Filing Date

09/17/2003

First Named Inventor

FRAMPTON E ELLIS

Art Unit

2153

Examiner Name

## NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
*	A	NONE, "PC vision: Intel unveils plans to bring PCs to vehicles", EDGE: Work Group Computing Report, EDGE Publishing, p. 1-2, ( October 28, 1996).	<input checked="" type="checkbox"/>
	B	LI, YAO, "Free-space Optical Bus-based WDMA Interconnects for Parallel Computation," LEOS '92 Conference Proceedings, Lasers and Electron-Optics Society Annual Meeting, p. 588-589, (November 16-19, 1992).	<input checked="" type="checkbox"/>
	C	DICKINSON ET AL, "An Integrated Free Space Optical Bus," 1989 IEEE International Conference on Computer Design" VLSI In Computers and Processors, p. 62-65, ( October 2-4, 1989).	<input checked="" type="checkbox"/>
	D	NATARAJAN ET AL, "Bi-Directional Optical Backplane Bus for General Purpose Multi-Processor," Journal of Lightwave Technology, Vol. 13 ( No. 6), p. 1031-1040, ( June 6, 1995).	<input checked="" type="checkbox"/>
	E	ZHAO ET AL, "General Purpose Bidirectional Optical Backplane: High Performance Bus for Multiprocessor Systems," Massively Parallel Processing Using Optical Interconnections, 2nd International Conference, p. 188-195, ( October 23-24, 1995).	<input checked="" type="checkbox"/>
	F	WU ET AL, "Microprocessor Control Signal Transmission through Optical Fiber," Conference Record of 1992 IEEE Industry Applications Society Annual Meeting, p. 1747-1750, ( October 4-9, 1992).	<input checked="" type="checkbox"/>

Examiner Signature	/Lashanya Nash/ (07/14/2010)	Date Considered	
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